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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/674,397	10/01/2003	Tadayoshi Ueda	R2180.0181/P181	4415
24998	7590	01/11/2007		EXAMINER
DICKSTEIN SHAPIRO LLP 1825 EYE STREET NW Washington, DC 20006-5403				RAHMAN, FAHMIDA
			ART UNIT	PAPER NUMBER
				2116
SHORTENED STATUTORY PERIOD OF RESPONSE		MAIL DATE	DELIVERY MODE	
3 MONTHS		01/11/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No.	Applicant(s)	
	10/674,397	UEDA, TADAYOSHI	
	Examiner	Art Unit	
	Fahmida Rahman	2116	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 19 October 2006.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-4 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) 1-3 is/are allowed.

6) Claim(s) 4 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____

DETAILED ACTION

1. This final action is in response to communications filed on 10/19/06.
2. Claims 1 and 4 have been amended, no new claims have been added, no claims have been canceled. Thus, claims 1-4 are pending.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claim 4 is rejected under 35 U.S.C. 102(b) as being anticipated by Ohmae (US patent 5237698).

For claim 4, Ohmae teaches the following limitations: **A method for supplying power to a CPU providing a power saving mode (Fig 1), said method comprising the steps of: providing a reset signal for resetting the CPU when an output voltage from a power supply (8) is equal to a prescribed operable level** (the reset signal is provided from 12 to the output of AND gate 4 for resetting CPU when output voltage from 8 is equal to a level which is able to make output of 6 as "1". Only when output of 6 is "1", the reset signal produced by 5 would be transmitted to "R" input of CPU. Therefore, prescribed operable level is that level of the voltage in the circuit of Fig 1,

which can produce "1" in output of 6; lines 34-63 of column 3); **setting a power saving mode** ("standby mode" or low voltage mode); **decreasing the prescribed operable level** (the operable level is decreased when output of 6 is "0", i.e., standby mode. Therefore, prescribed level of circuit of Fig 1 is decreased when the output of 6 is no longer "1") **before decreasing the output voltage down to a power saving level** (the output voltage can be down to 2V or lower (lines 60-61 of column 3). Take 1.9V is the power saving level. The prescribed level is decreased, i.e., output of 6 is decreased from "1" to "0", before output of 8 becomes 1.9V as output of 6 is "0" when the circuit is set to standby mode, where the output of power supply is 3V (lines 19-20 of column 3)) **such that the reset signal is not provided when setting the power saving mode** (the reset signal is not provided to CPU when output of 6 is "0"); **resetting the power saving mode** ("operation mode"); **and recovering the prescribed operable level** (the level required to maintain output of 6 as "1" is recovered in normal operation mode) **after recovering the output voltage** (the output voltage is recovered from lower than 2V to ON state first and then the operable level is recovered to produce output of 6 as "1" as mentioned in lines 25-30 and lines 38-43 of column 4).

Allowable Subject Matter

4. Claims 1, 2 and 3 are allowed.

Response to Arguments

Applicant's arguments filed on 10/19/06 with respect to claim 4 have been fully considered but they are not persuasive.

Applicant argues that Ohmae fails to disclose "decreasing the prescribed operable level before decreasing the output voltage down to a power saving level such that the reset signal is not provided when setting the power saving mode".

Examiner disagrees. Ohmae teaches decreasing the prescribed operable level (the prescribed level of circuit of Fig 1 is decreased when the output of 6 is no longer "1"; the prescribed level is decreased in the sense that output of 6 is decreased from "1" to "0" in standby mode) before decreasing the output voltage down to a power saving level (the output voltage can be down to 2V or lower. Take 1.9V is the power saving level. The prescribed level is decreased, i.e., output of 6 is decreased from "1" to "0", before output of 8 decreases to 1.9V, as output of 6 is "0" when the circuit is set to standby mode, where the output of power supply is 3V (lines 19-20 of column 3)).

Applicant further argues that Ohmae fails to disclose "recovering the prescribed operable level after recovering the output voltage".

Examiner disagrees. Ohmae teaches recovering the prescribed operable level (the level required to maintain output of 6 as "1" is recovered in normal operation mode) after

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recovering the output voltage (the output voltage is recovered as soon as battery is reloaded as mentioned in lines 30-52 of column 4. The output voltage is recovered from lower than 2V to ON state first and then the operable level is recovered to produce output of 6 "1" as mentioned in lines 25-30 and lines 38-43 of column 4. Therefore, output voltage is recovered first and then the prescribed operable level is recovered).

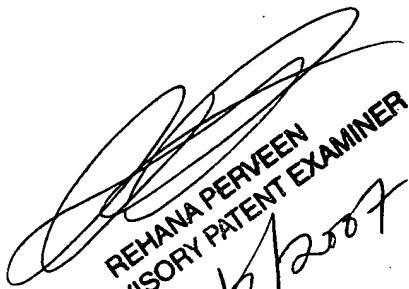
Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a). A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fahmida Rahman whose telephone number is 571-272-8159. The examiner can normally be reached on Monday through Friday 8:30 - 5:30.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on 571-272-3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Fahmida Rahman
Examiner
Art Unit 2116



REHANA PERVEEN
SUPERVISORY PATENT EXAMINER
1/8/2017